

ABSTRACT OF THE DISCLOSURE

A method for performing DMA transfers with dynamic descriptor structure. A processor first creates a new chain of descriptors each including an end-of-chain (EOC) entry set to a false value except a dummy descriptor at the end of the new chain having the EOC entry set to a true value. The new descriptor chain can be appended to a previous descriptor chain, if any, by transferring parameters and a link pointer of the first descriptor within the new descriptor chain to a dummy descriptor of the previous descriptor chain. Then the processor changes the EOC entry of the dummy descriptor within the previous chain from the true value to the false value. Therefore, a DMA controller is able to transfer data in accordance with the new descriptor and also the previous one.